

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/815,478	BALL, JAMES LORAN	
		Examiner	Art Unit	Page 1 of 1
		Vincent Lai	2181	

**U.S. PATENT DOCUMENTS**

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-			
B	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages	
U	"Data Alignment When Migrating to 64-bit Intel Architecture," Intel.	
V		
W		
X		

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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